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Diamond and Refractory Coatings of Gun Barrels
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Material surfaces of gun barrels in electrothermal-chemical launchers are subject to high heat fluxes, which may exceed 100 GW/m² over 0.01 - 5 ms pulse duration. Such high heat fluxes result in several deforming processes of barrel material, which reduce the efficiency and lifetime of the launcher. The successful operation requires minimum erosion over a large number of repetitive exposures. Diamond and refractory coated barrel material surfaces have been exposed to high heat fluxes from 2 to 60 GW/m² over 100 μ s duration using the electrothermal launcher, SIRENS. The high heat fluxes produced by SIRENS plasma are primarily from blackbody spectrum photons. The plasma boundary layer "vapor shield" formed by surface vaporization helps to reduce heat transport to the surface. Coatings have been prepared by different methods (sputtering, molten salt, electroplating, chemical vapor deposition and low pressure plasma spray). Surface erosion is caused primarily by convection and radiation emitted from the plasma. The better performance of coated surfaces is attributed to the better thermal conductivity of the heat sink (substrate). Preliminary tests of diamond coating on silicon substrates were not successful because the wafers were destroyed, especially at higher values of incident heat fluxes. SEM micrographs showed that diamond coating has been removed from the silicon surface due to the thermal shock under high pressure.

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ABSTRACT

Material surfaces of gun barrels in electrothermal-chemical launchers are subject to high heat fluxes, which may exceed 100 GW/m² over 0.01 - 5 ms pulse duration. Such high heat fluxes result in several deforming processes of barrel material, which reduce the efficiency and lifetime of the launcher. The successful operation requires minimum erosion over a large number of repetitive exposures. Diamond and refractory coated barrel material surfaces have been exposed to high heat fluxes from 2 to 60 GW/m² over 100 μ s duration using the electrothermal launcher, SIRENS. The high heat fluxes produced by SIRENS plasma are primarily from blackbody spectrum photons. The plasma boundary layer "vapor shield" formed by surface vaporization has large temperature and density gradients. The produced vapor due to ablation is injected into that boundary layer and thus the layer thickness increases and its temperature decreases, which results in less heat transported to the surface. The developed pressures in the vapor shield (\geq 1kbar) are large enough to expand against incoming plasma flux, so much of the stored internal energy of the vapor will be propagated away from the surface. Coatings have been prepared by different methods (sputtering, molten salt, electroplating, chemical vapor deposition and low pressure plasma spray). Surface erosion is caused primarily by convection and radiation emitted from the plasma. Mo/Cu has approximately 40% less erosion than pure molybdenum. Ta/Cu showed the lowest erosion, about 88% less than pure molybdenum, while W/Cu and W/Mo have approximately no erosion. Tantalum and tungsten coatings are the most promising among other refractory materials. The better performance of coated surfaces is attributed to the better thermal conductivity of the heat sink (substrate). In case of Mo/Cu the heat sink absorbs \approx 60% of the heat, while for Ta/Cu, the heat sink absorbs most of the heat (\approx 98%) so that surface erosion is negligible (\approx 0.6 μ m at 33 GW/m²). Preliminary tests of 10-20 μ m diamond coating on silicon substrates were not successful because the wafers were destroyed (0.5-3.3 mm thickness silicon wafers are very brittle), especially at higher values of incident heat fluxes. SEM micrographs showed that diamond coating has been removed from the silicon surface due to the thermal shock under high pressure.

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INTRODUCTION

The heat fluxes generated during the operation of electrothermal-chemical launchers (ETC) exceed 10 GW/m^2 for a duration of 0.01 - 5 ms, which results in the damage of the launcher components due to erosion. Erosion of the launcher critical components represents a limiting factor that affects the launcher's efficiency, and decreases its lifetime [1-5]. Successful operation of ETC launchers requires minimum erosion of the launcher components. Refractory, or refractory-coated, materials and alloys may help in reducing the erosion of barrels, while ablation of the capillary insulator may be reduced by using high tensile insulating materials [1-2]. Several materials and microcomposites (e.g. CuNb) [6] have been tested to understand their erosion behavior under typical operational conditions experienced in ETC launchers [7-9]. Diamond coatings have been suggested due to the high ablation threshold of diamond surfaces under pulsed high heat loading [10-11]. The plasma boundary layer adjacent to the eroding surface, is characterized by large temperature and density gradients. The vapor produced due to ablation is injected into that boundary layer and thus the layer thickness increases and its temperature decreases, which results in less heat transport to the surface. Such boundary layer (vapor shield) helps to reduce the surface erosion by absorbing a fraction of the incident energy [5,7,10]. The heat flux q'' that reaches the surface is represented by $q'' = f S$, where f is the energy transmission factor through the vapor layer, and S is the incident heat fluence. Absorbed energy appears as vapor shield internal energy which can be transported away from the localized area. Additional surface protection may be achieved via coatings on material surfaces with high thermal conductivity to reduce surface heat flux due to the effectiveness of the heat sink [10].

EXPERIMENTAL DEVICE AND DIAGNOSTICS ARRANGEMENT

The experimental device is an electrothermal launcher SIRENS [12,13], which produces high-density (10^{25} - 10^{26} /m^3) low-temperature (1-3 eV) plasma. The plasma is formed in the source section (capillary) by the ablation of the source insulator (Lexan). SIRENS operates either under vacuum (fuseless operation) or at atmospheric pressure (fuse operation). Table I shows SIRENS operational characteristics (fuseless), while Fig. 1 shows the conceptual design of the launcher with several barrel diagnostics.

TABLE I
SIRENS OPERATIONAL CHARACTERISTICS

Discharge voltage (10 kV max.)	1 - 8 kV
Peak current (100 kA max.)	20-100 kA
Net Input Energy (15 kJ max.)	1 - 8 kJ
Discharge period	100-140 μs
Radiated Power (120 GW/m^2 max.)	2-70 GW/m^2
Peak pressure	> 1 kbar
Plasma density	10^{25} - 10^{26} m^{-3}
Peak plasma temperature	4 - 6 eV
Average plasma temperature	1 - 3 eV
Average plasma velocity (free expansion)	$\approx 12 \text{ km/sec}$

Material samples are exposed to SIRENS plasma. A fast response thermocouple is attached to the back of the sample to monitor the temperature history. The device has various diagnostics arrangements as shown in Fig. 1. Material diagnostics include the weight loss (microbalance), Scanning Electron Microscopy (SEM), Energy Dissipative X-ray Analysis (EDXA), and Auger Electron Spectroscopy (AES). Signals are digitized via two 8-channel LeCroy waveform digitizers.

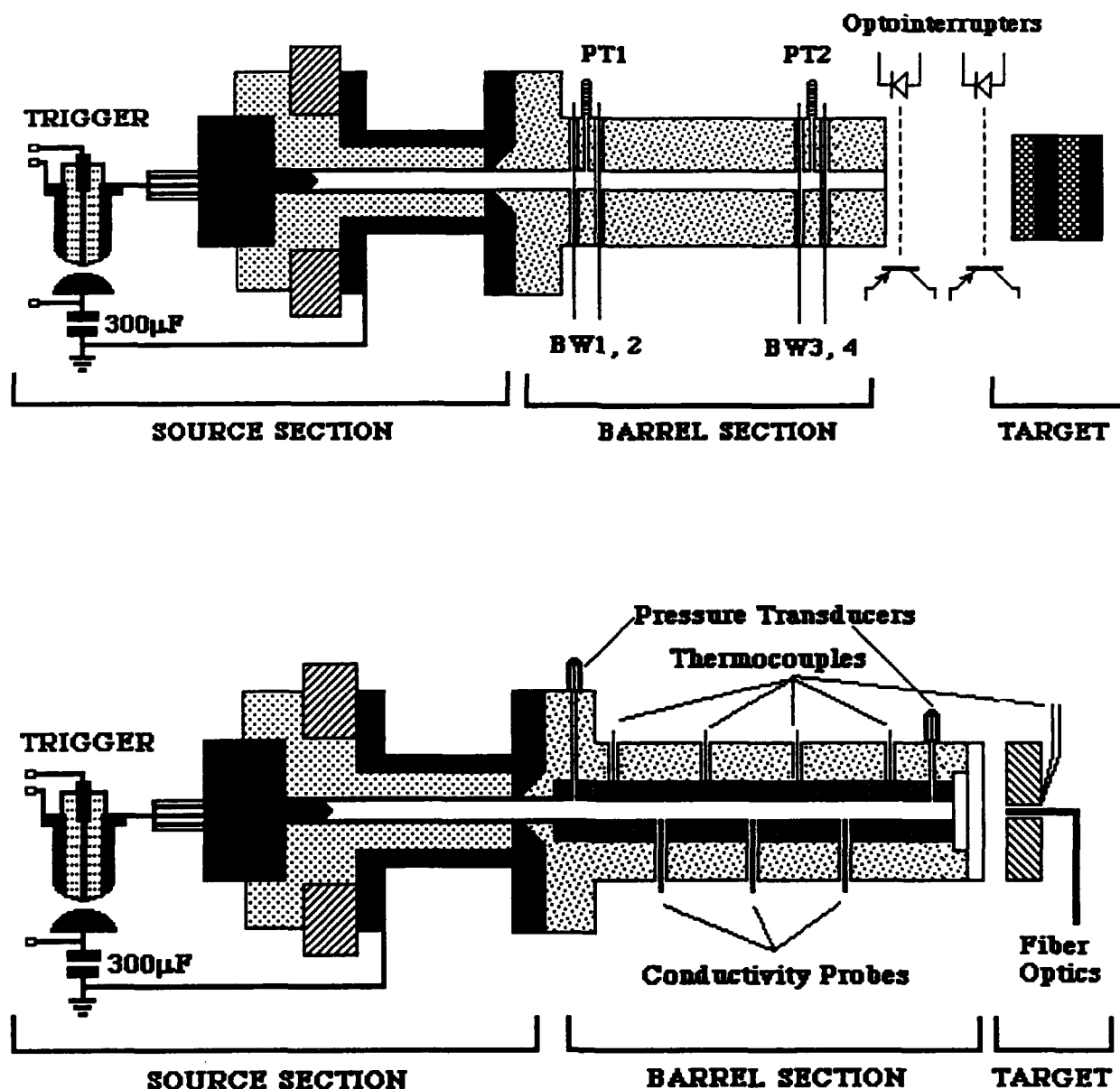


Fig. 1 Schematic drawing of the electrothermal launcher SIRENS showing the plasma source (capillary) and the barrel sections. Several diagnostics can be used. Top arrangement shows pressure transducers (PT1, PT2), break wires (BW1,2,3 and 4), and optointerrupters. Bottom arrangement shows conductivity probes, pressure transducers, and thermocouples.

EXPERIMENTAL RESULTS OF MATERIAL SURFACE EROSION

Several candidate materials coatings and alloys have been proposed as potential components for ETC launchers. Material surfaces were exposed to high heat fluxes up to 70 GW/m² over 100 μ s (7 MJ/m²) in SIRENS. Coated surfaces were prepared by Benét laboratory on standard copper substrates (6.35 mm diameter x 3.175 mm thick) using various coating techniques, with 25 to 37 μ m coating thickness. Tungsten coatings were prepared by UCLA on molybdenum and copper substrates (25.4 mm diameter x 1 mm thick), with 1mm coating thickness. Diamond coatings were prepared at the NCSU Materials Engineering Laboratories on single crystal silicon wafers (25.4 mm diameter x 0.5 or 3.3 mm thick), with 10-20 μ m coating thickness [14]. Samples were exposed to high heat fluxes from 2 to 70 GW/m² over 100 μ s (0.2 to 7 MJ/m² heat fluence). Material samples were ultrasonically cleaned before and after the exposure, and the erosion depths were calculated from weight loss measurements. SEM, EDXA and AES were conducted at NCSU Materials Engineering Analytical Facilities.

The erosion depths Δx of the tested material surfaces have been calculated from the weight loss. Fig. 2 shows a comparison between different coatings on copper exposed to 33 GW/m² incident heat flux over 100 μ s. Fig. 3 shows the measured erosion depths of several metals tested under the same experimental conditions for purpose of comparison between coated and uncoated surfaces. It is clear, from Fig.2, that the lowest erosion is that of electroplated chromium on copper, sputtered tantalum on copper and sputtered tantalum (with 10% tungsten) on copper coating. LC chromium sputtered on copper has higher erosion, a factor of 15, than electroplated LC Cr. Sputtered HC Cr has also higher erosion than electroplated HC Cr, but only a factor of 5.

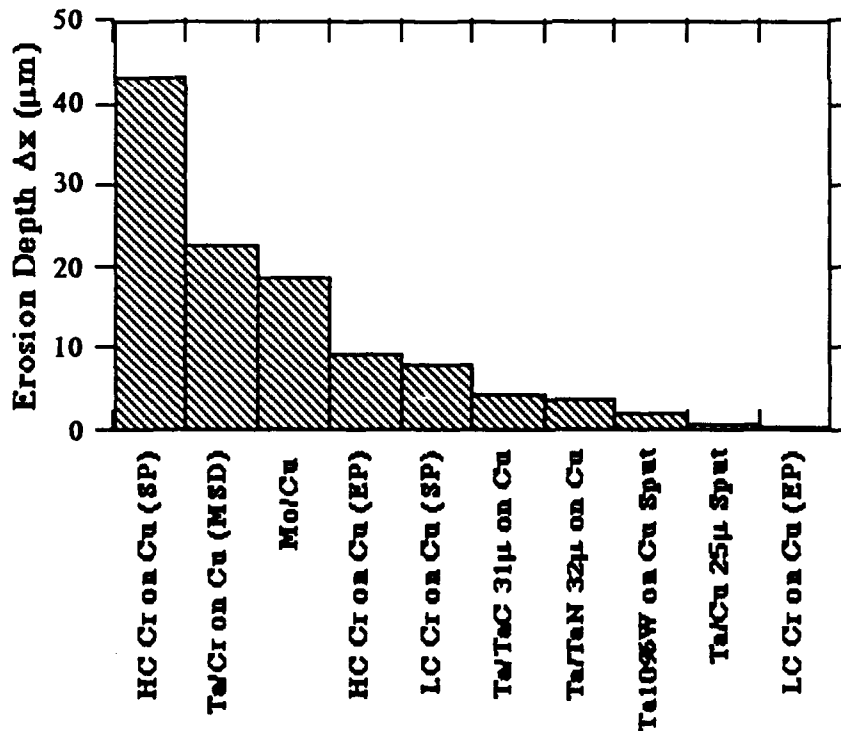


Fig. 2 Erosion depths of coated material surfaces exposed to SIRENS plasma, at 33 GW/m² incident heat flux over 100 μ s. SP = sputtered, MSD = molten salt deposition, EP = electroplated.

Such comparison is suggestive that electroplating is better than sputter deposition. This may be attributed to the better contact resistance of electroplated surfaces, which helps for a better heat transfer to the copper bulk to act as an effective heat sink. Fig. 4 shows erosion of three alloys, where tungsten-rhenium metal has approximately no erosion, while HD-17 tungsten alloy has considerably low erosion compared to glidcop (a factor 6 less). A comparison between all the tested samples shows that molten salt deposition (MSD) is not a favorable coating process. Sputter deposition, in general, showed good performance where the contact resistance allows for good heat conduction to the substrate. Electroplating has always good contact resistance and uniform deposition.

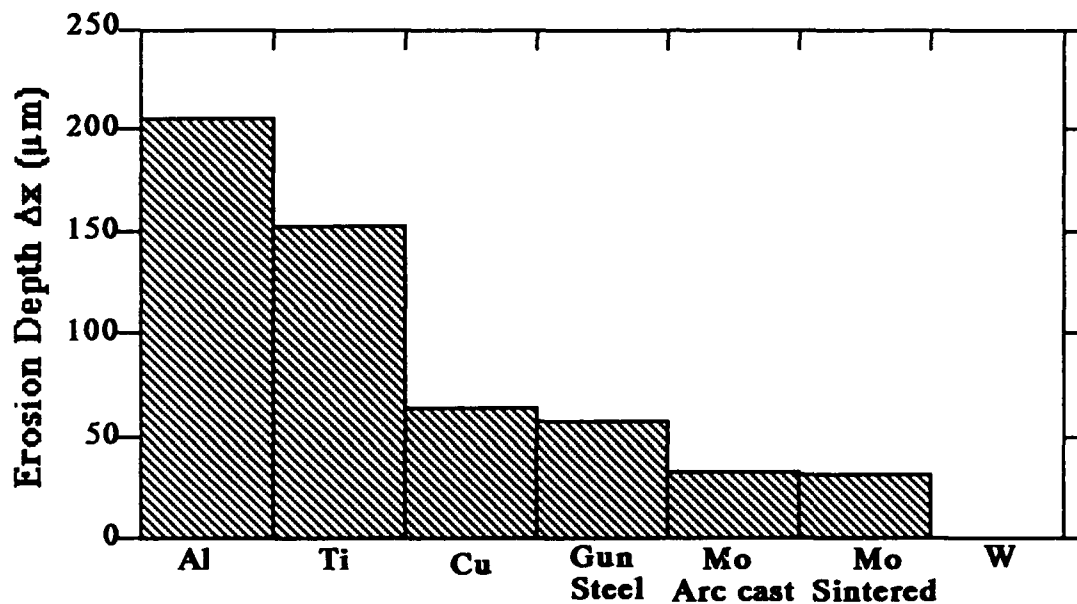


Fig. 3 Erosion depths of pure metal surfaces exposed to SIRENS plasma, at 33 GW/m² incident heat flux over 100 μs .

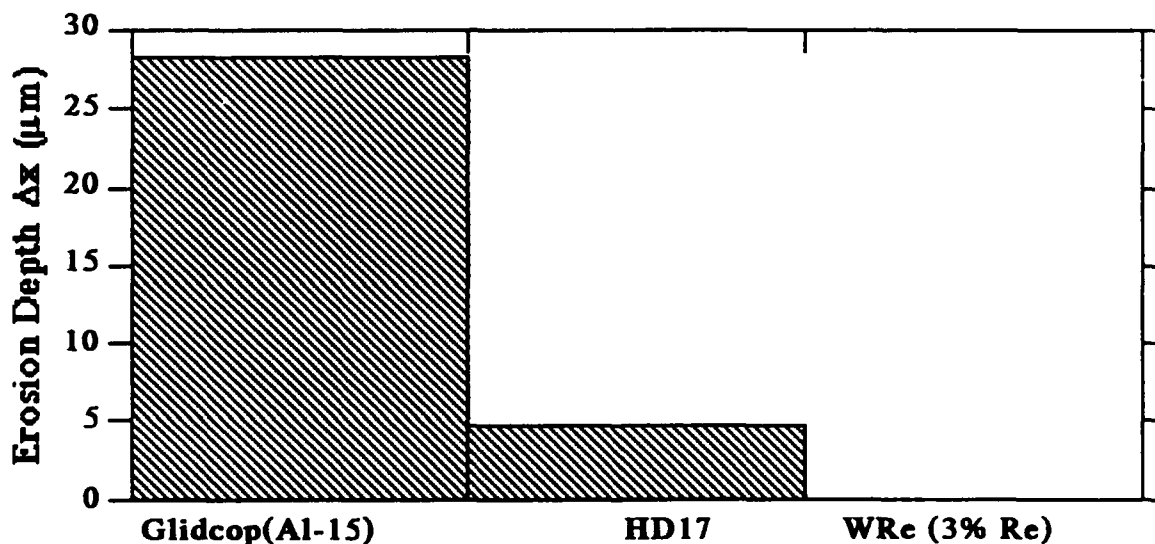


Fig. 4 Erosion depths of several alloys exposed to SIRENS plasma, at 33 GW/m² incident heat flux over 100 μs . Glidcop (Al-15) is pure copper alloyed with aluminum oxide (0.15% Al_2O_3), HD17 is a tungsten alloy (90% W, 6% Ni and 4% Cu), WRe is a tungsten rhenium metal (3% Re).

Calculated erosion thickness for chromium, considering full energy deposition, yields 59.5 μm . The experimental results show that erosion thickness is much less for all cases of chromium coating. The experimental erosion values vary from about 1% to 70% of the calculated ones. About 1% for electroplated LC Cr, 16% for electroplated HC Cr, 14% for sputtered LC Cr, and 72% for HC Cr. Surface erosion of electroplated LC Cr is approximately equal to that of sputtered HC Cr. The reduced erosion effect is not only due to the copper heat sink, but also due to the vapor shield effect. Fig. 5 shows a comparison between calculated (with and without heat sink) and measured erosion thickness for tungsten coatings on molybdenum and copper, and molybdenum and tantalum coatings on copper. Calculated erosion with heat sink for coatings on copper substrates correlates well with measured values (W/Cu, Mo/Cu and Ta/Cu).

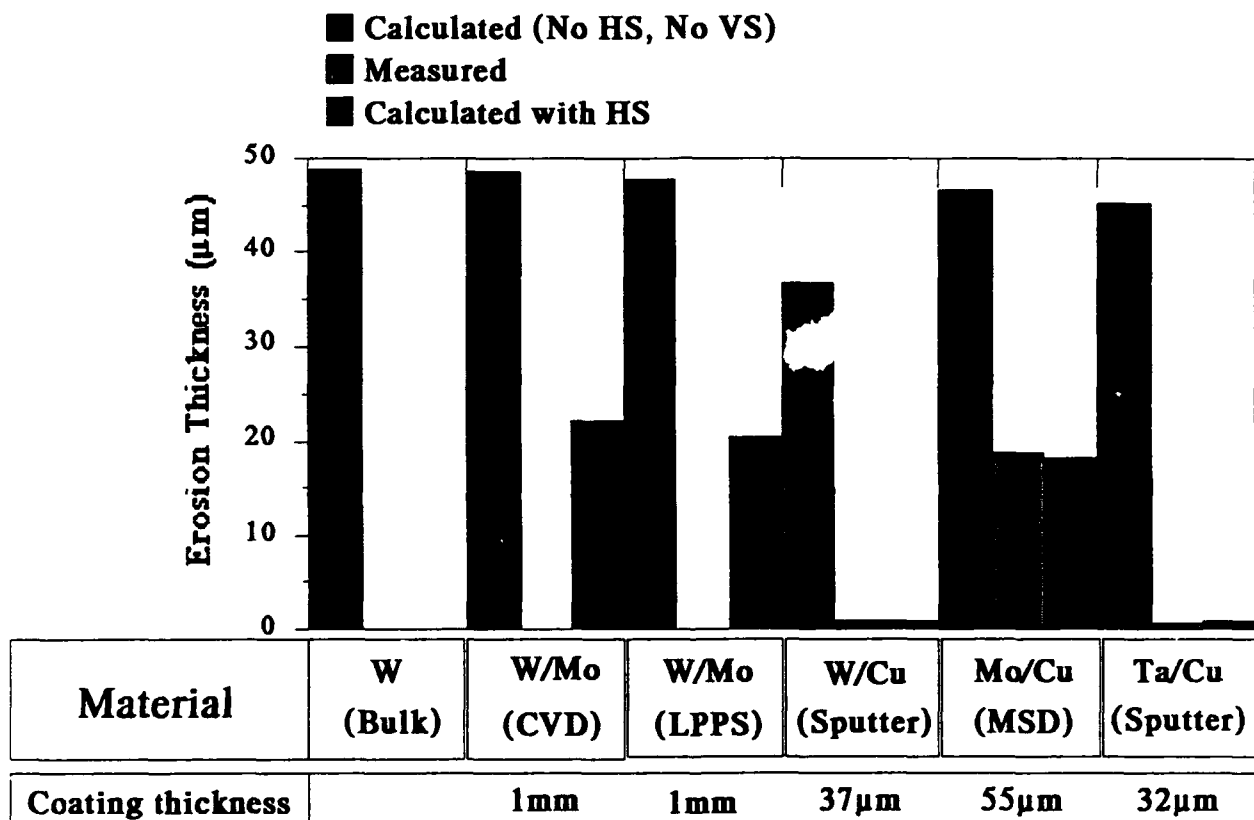


Fig. 5 Calculated and measured erosion (33 GW/m^2 , 0.1 ms) of pure tungsten, tungsten coating on molybdenum and copper, and molybdenum and tantalum coating on copper. CVD = Chemical Vapor Deposition, LPPS = Low Pressure Plasma Spray, MSD = Molten Salt Deposition. HS and VS stand for Heat Sink and Vapor Shield, respectively.

Ablation studies have been conducted on some insulators commonly used in ETC launchers, at the same incident heat flux of 33 GW/m^2 . Fig. 6 shows ablation depths of Lexan, boron nitride, aluminum oxide and glass-bonded mica. Although glass-bonded mica has the lowest ablation depth but melting of the glass-bond may take place at higher heat fluxes. Boron nitride becomes abrasive due to phase transformation from the hexagonal plate structure to the cubic diamond-like structure under the influence of high pressure. Lexan and aluminum oxide have the most uniform ablation under high heat loading and high pressures. The vapor shield effect is very efficient in protecting surface ablation of insulating materials. A comparison between

the energy transmission factor through the boundary layer "vapor shield" is shown in Fig. 7 for Lexan, boron nitride and aluminum oxide. The energy transmission factor through the vapor shield varies between 17 and 25 % for the tested insulators, and decreases to about 5% at incident heat fluxes between 30 and 44 GW/m². Fig. 7 shows the variation of the energy transmission factor f with the incident heat flux. The energy transmission factor has been calculated from ablation measurements (experimental data are related to the heat flux reaching the ablating surface) and compared to the source fluence. Although the energy transmission factor for boron nitride is about a factor of 2 less than that of Lexan, the abrasive behavior of boron nitride at higher pressures is undesirable. Aluminum oxide is approximately a factor of 2 less than Lexan at lower values of incident heat fluxes, but they reach the same behavior at higher values.

Erosion studies have been conducted on diamond coatings on single crystal silicon wafers. Two sets of wafers were used, 0.5 mm and 3.3 mm thickness x 25.4 mm diameter. Diamond films were developed by chemical vapor deposition in 2% CH₄ atmosphere at 55 Torr pressure. Film thickness varies between 10 to 20 μm depending upon the total number of scans and processing time (26 to 36 hours). The first two samples were exposed to 33 GW/m² incident heat flux (5 kJ input energy to SIRENS capillary at 0.75 kbar capillary pressure). Both samples were destroyed with full removal of the diamond coating from the surface of the silicon wafer. Three samples were exposed at lower heat fluxes (4,2 and 1 GW/m²), where the source pressure is less than 0.1 kbar. All samples were also destroyed and weight loss could not be obtained. Only surface analysis could be conducted, which revealed that only the sample exposed at 1 GW/m² still has some diamond features on the surface. Fig. 8 shows a sample exposed to 33 GW/m², and Fig. 9 shows the one exposed to 1 GW/m². The top micrograph represents the surface before exposure, while the bottom one is for the same surface after exposure. It is clear from Fig. 8 that diamond coating has been totally removed. The exposed surface of Fig. 9 shows the partial removal and distortion of the diamond film. Stronger material surfaces, other than silicon, are currently under investigation (silicon nitride, tungsten carbide, and possibly Lexan) for potential diamond coating.

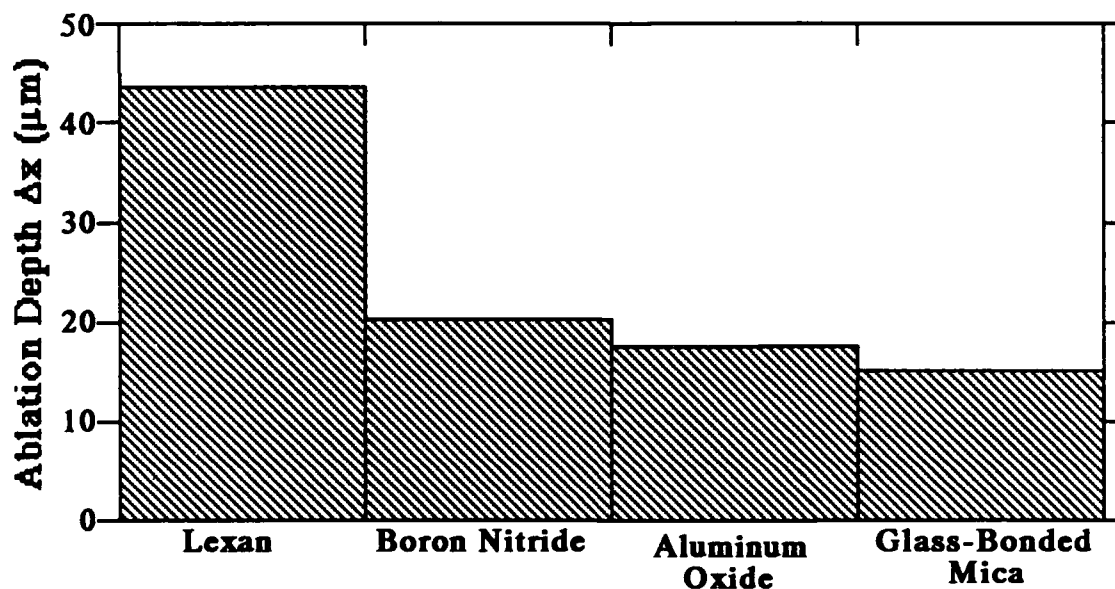


Fig. 6 Ablation depths of insulators commonly used in ETC launchers, exposed to SIRENS plasma at 33 GW/m² incident heat flux over 100 μs .

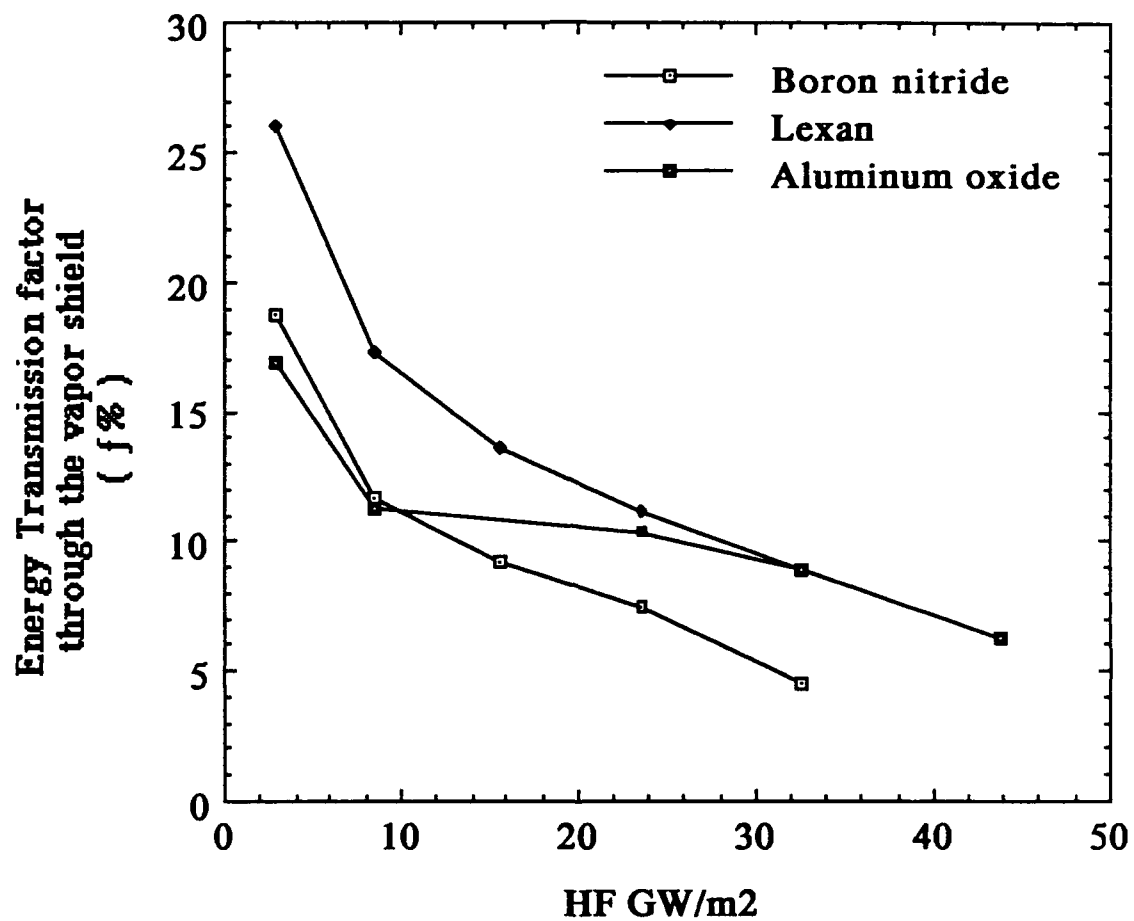


Fig. 7 The energy transmission factor f through the vapor shield for the tested insulating materials as a function of the incident heat flux.

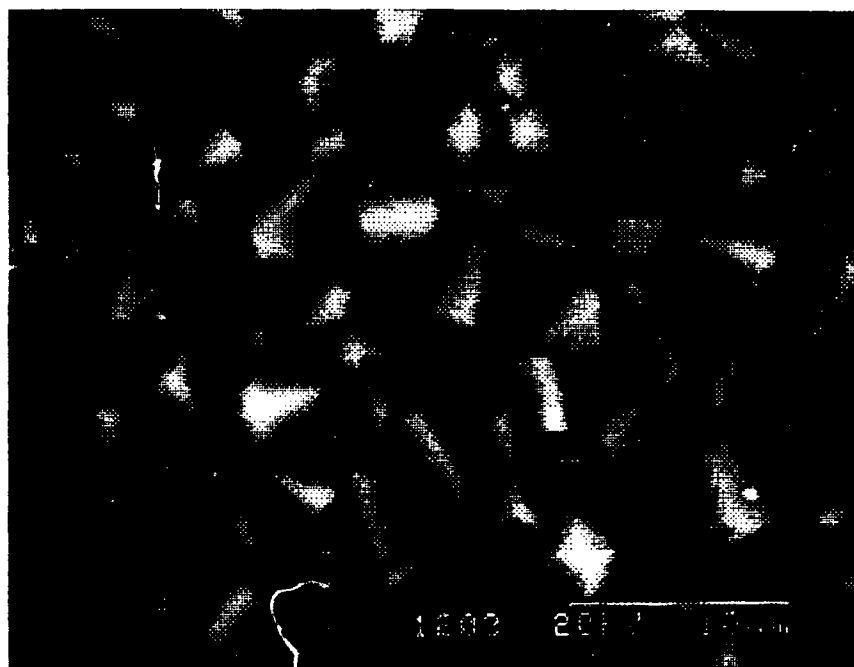


Unexposed sample, magnification 5000x

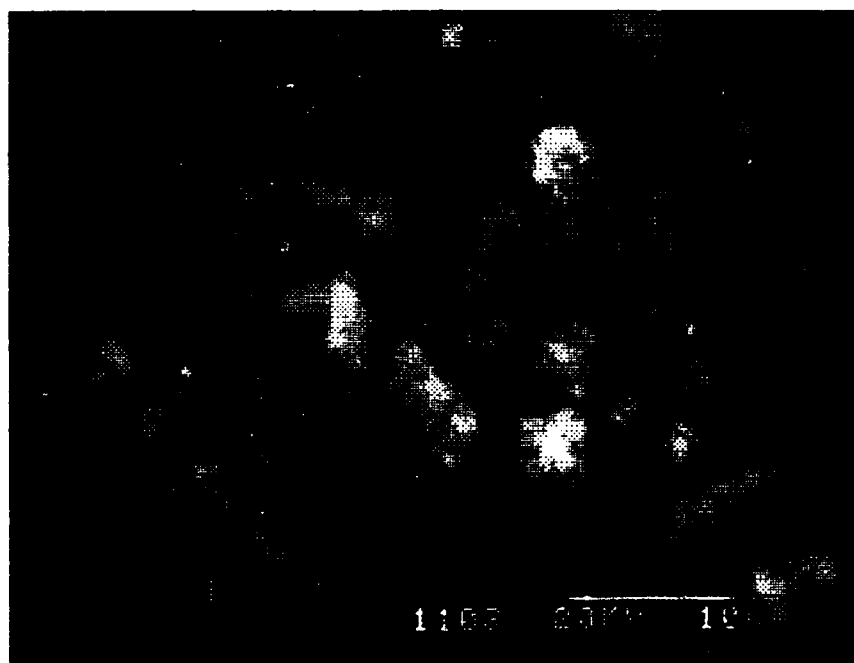


Exposed sample, magnification 2000x

Fig. 8 SEM micrographs of unexposed (top) and exposed (bottom) diamond coating on a single crystal silicon wafer (10-13 μm diamond coating thickness on 25.4 mm diameter x 0.5 mm thick single crystal silicon wafer, coating processing parameters: 2% CH_4 , 55 Torr, 300 scans, 26 hours). Shot # 0415, Sample # W26, 33 GW/m^2 incident heat flux.



Unexposed sample, magnification 3000x



Exposed sample, edge of impact, magnification 3000x

Fig. 9 SEM micrographs of unexposed (top) and exposed (bottom) diamond coating on a single crystal silicon wafer (20 μm diamond coating thickness on 25.4 mm diameter x 3.3 mm thick single crystal silicon wafer, coating processing parameters: 2% CH_4 , 55 Torr, 420 scans, 36 hours). Shot # 0471, Sample # W-12N, 1 GW/m^2 incident heat flux.

CONCLUSIONS

The erosion of diamond, several candidate coatings and insulator materials have been studied under pulsed high heat fluxes produced in the SIRENS ET launcher for potential applications in ETC launchers. Surfaces were exposed to a standard 33 GW/m² heat flux over 100 μ s at 0.75 kbar capillary pressure. The lowest erosion noted was for electroplated chromium, sputtered tantalum and sputtered tantalum (with 10% tungsten) coatings on copper. Molten salt deposition showed the worst coating results. Sputter deposition showed good performance where the contact resistance allows for good heat conduction to the substrate. Electroplating has the best contact resistance and uniform deposition. Tested alloys showed that tungsten-rhenium metal has approximately no erosion, while HD-17 tungsten alloy has low erosion compared to glidcop. It has been shown that a good heat sink, with good thermal conductivity, helps to reduce surface erosion. Glass-bonded mica does not withstand higher heat fluxes due to melting of the glass-bond structure. Boron nitride becomes abrasive, most probably due to a phase transformation from the hexagonal plate structure to the cubic diamond-like structure under the influence of high pressure. Lexan and aluminum oxide have the most uniform ablation under high heat loading and high pressures. The vapor shield effect is very efficient in protecting surface ablation of insulating materials. The energy transmission factor through the vapor shield varies between 17 and 25 %, and decreases to about 5% at incident heat fluxes between 30 and 44 GW/m². Tests conducted on diamond coating were not successful because the silicon wafers were destroyed (0.5-3.3 mm thickness silicon wafers are very brittle), especially at higher values of incident heat fluxes. SEM micrographs showed that diamond coating had been removed from the silicon surface due to the thermal shock under high pressure. Stronger material surfaces, other than silicon, are currently under investigation (silicon nitride, tungsten carbide, and possibly Lexan) for potential diamond coating.

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